

ABOUT THE COLLEGE

Dr. Mahalingam College of Engineering and Technology (MCET) was established in the year 1998 by Dr.M.MANICKAM with a view to commemorate the 75th birth day of his beloved father Arutchelvar Dr. N. MAHALINGAM with a mission to impart high quality competency based education in Engineering & Technology to the younger generation to acquire the required skills and abilities to face the challenging needs of the industry around the globe. MCET is a self-financing, co-educational Autonomous Engineering College and it is approved by All India Council for Technical Education (AICTE), New Delhi & affiliated to Anna University, Chennai. The MCET currently offers 10 UG, 6 PG and 5 Doctoral programmes in Engineering and Technology. Among 10 UG programmes CSE, EEE, ECE, Mech, Auto, Civil and EIE has been accredited by the National Board of Accreditation (NBA) and the institute is accredited by NAAC with 'A++' grade.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering was established in the year 1998 and offers B.E (ECE) and M.E (Communication Systems) approved by AICTE, affiliated to Anna University, and accredited by NBA. Also our department is affiliated by Anna University, Chennai as research Centre to pursue Ph.D. The department has experienced and qualified faculty members who are fully committed to teaching, research and dedicated to impart quality education to the students. The students have access to well equipped, state-of-the-art laboratories. The department is focusing on research activities in the predominant areas viz., RF, VLSI, Image & Signal processing, Data analytics, Communication and Networking.

Chief Patron:

Dr. M. Manickam
Chairman, MCET

Patron:

Shri. M. Hari Hara Sudhan
Correspondent, MCET

Chairman:

Dr. C. Ramaswamy
Secretary, NIA Educational Institutions

Co- Chairman:

Dr. A. Rathinavelu
Principal, MCET

Convenors:

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Professor and Head - ECE
Dr.K.N. Vijeyakumar
Associate Professor - ECE

Organizing Secretaries:

Dr.V.K.Sudha
Professor - ECE
Dr.S.Bharathi
Assistant Professor (SS) - ECE

Committee Members:

Dr.C.Kalamani, AP (SS) - ECE
Ms.M.Senthilarasi, AP (SS) - ECE
Mr. D.Parthiban, AP - ECE
Mr.V.Gurunathan, AP - ECE
Ms.S.Kalaiselvi, AP - ECE
Mr.A.Shafeek, AP - ECE

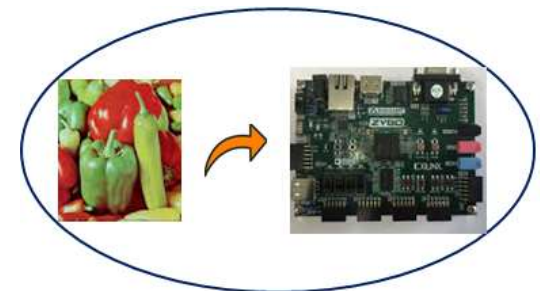


Two days' Workshop on "MODELING AND IMPLEMENTATION OF IMAGE PROCESSING ALGORITHMS IN FPGA HARDWARE"

11th & 12th October 2019

Organized by

**Department of Electronics and
Communication Engineering**



**Dr.MAHALINGAM COLLEGE OF
ENGINEERING AND TECHNOLOGY**

(An Autonomous Institution)
(Accredited by NBA & NAAC
with 'A++' Grade)
Pollachi-642003

Coimbatore (Dt), Tamilnadu
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ABOUT THE WORKSHOP

This workshop is designed to develop a practical knowledge on implementation of various Image processing algorithms in FPGA hardware. FPGAs are often used as implementation platforms for image processing applications because of their unique structure. This workshop comprises a combination of theory and hands on training. The ever growing demand of implementation of image processing algorithms in hardware promises definite solutions to various engineering applications. Since the workshop includes practical sessions, students, faculties and researchers can expand and deploy their ideas in real time with the help of Verilog HDL programming in FPGA. The topics to be covered in the workshop are as follows:

Day 1

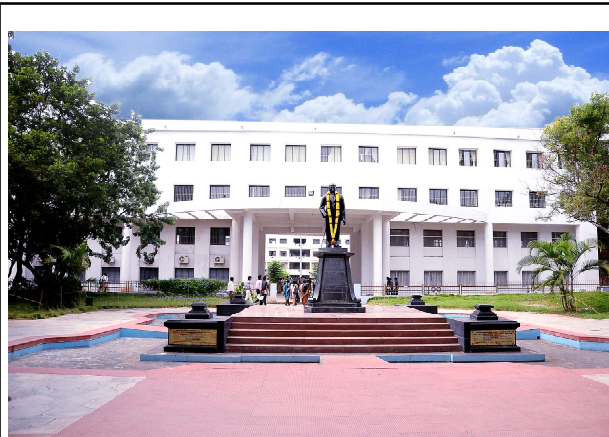
- ★ Introduction to Computer Vision
- ★ Role of Classifiers for Digital Images
- ★ From handcrafted features to deep learning
- ★ Hands on session on Implementation of Image processing algorithms in MATLAB

Day 2

- ★ Verilog HDL design for Image Processing algorithms
- ★ Concept of pipelining and parallel processing
- ★ Approximate logic in architecture design
- ★ Hands on session on Implementation of Image Processing algorithms in FPGA hardware

PROGRAMME EXPERTS

Sessions will be handled by Professionals from reputed Institutions.



ADDRESS FOR COMMUNICATION

Dr.S.Bharathi

Assistant Professor (SS),
Department of ECE,

Dr. Mahalingam College of
Engineering and Technology,
Pollachi - 642003

Email: sbharathi@drmcet.ac.in

Mobile Number: +919486410806, +919944324990

REGISTRATION FEE

Registration Fee	Amount
PG/UG Students/Faculty Members from Academic Institutions /Industries	Rs.500/-

The registration fee includes workshop kit, refreshment and lunch.

Two days' Workshop on "MODELING AND IMPLEMENTATION OF IMAGE PROCESSING ALGORITHMS IN FPGA HARDWARE"

11th & 12th October 2019

REGISTRATION FORM

Name: _____

Designation: _____

Department: _____

Institution/Organization: _____

Address: _____

Phone/Mobile: _____

E-Mail: _____

(DD should be drawn in favour of "**The Principal, Dr.Mahalingam College of Engineering and Technology, payable at "Pollachi"** for the amount of Rs.500/-towards registration fees).

DD Number _____

Date : _____

Bank : _____

Branch: _____

Place: _____ Signature of the candidate

"Send your Registration form on or before 28.09.2019"